

RST	ESP12E Electrodragon Board	TxD
ADC		RxD
CHPD		4
16		5
14		0
12		2
13		15
Vcc (5V)		GND
CS		9
DO		DI
10		CLK

Optional on board LDO for 3V3 power, 0.5-1A output (NOT for serial port and other IO pins), input voltage range 5-12V

1K resistor between CHPD and VCC, to enable chip

1K resistor between GPIO2 and GND

GPIO15 to GND for normal flash boot mode

On board Reset button and flashing firmware button (GPIO0). Press down the flashing firmware then press the reset will enter into firmware fleshing mode